



# SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

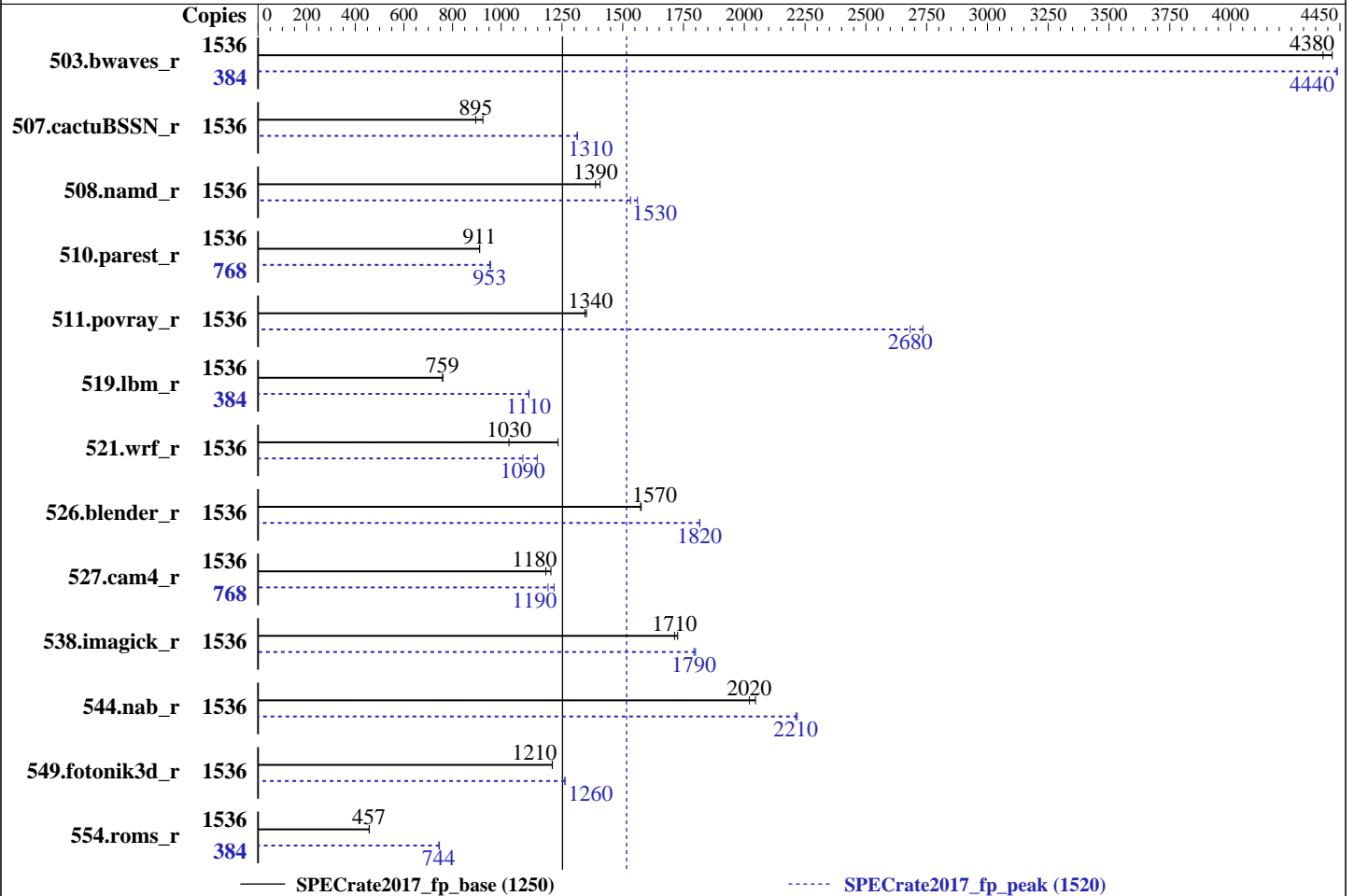
## Fujitsu Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017



### Hardware

CPU Name: SPARC64 XII  
 Max MHz.: 4350  
 Nominal: 4250  
 Enabled: 192 cores, 16 chips, 8 threads/core  
 Orderable: 1 to 16 BBs; each BB contains 1 or 2 CPU chips;  
 2, 3, 4, ... 384 cores  
 Cache L1: 64 KB I + 64 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 32 MB I+D on chip per chip  
 Other: None  
 Memory: 8704 GB (136 x 64 GB 4Rx4 PC4-2400T-R)  
 Storage: 1 x 600 GB 10K RPM SAS (for system disk)  
 Other: None

### Software

OS: Oracle Solaris 11.3 SRU 24.4  
 Compiler: C/C++/Fortran: Version 12.6 of Oracle Developer Studio  
 Parallel: No  
 Firmware: Fujitsu HCP Version 3040 released Oct-2017  
 File System: tmpfs  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other: None



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	1536	<b><u>3516</u></b>	<b><u>4380</u></b>	3486	4420			384	867	4440	<b><u>868</u></b>	<b><u>4440</u></b>		
507.cactuBSSN_r	1536	<b><u>2172</u></b>	<b><u>895</u></b>	2102	925			1536	<b><u>1482</u></b>	<b><u>1310</u></b>	1480	1310		
508.namd_r	1536	1037	1410	<b><u>1051</u></b>	<b><u>1390</u></b>			1536	<b><u>952</u></b>	<b><u>1530</u></b>	935	1560		
510.parest_r	1536	<b><u>4409</u></b>	<b><u>911</u></b>	4409	911			768	2101	956	<b><u>2108</u></b>	<b><u>953</u></b>		
511.povray_r	1536	<b><u>2668</u></b>	<b><u>1340</u></b>	2655	1350			1536	1312	2730	<b><u>1337</u></b>	<b><u>2680</u></b>		
519.lbm_r	1536	<b><u>2134</u></b>	<b><u>759</u></b>	2129	760			384	<b><u>363</u></b>	<b><u>1110</u></b>	363	1110		
521.wrf_r	1536	<b><u>3333</u></b>	<b><u>1030</u></b>	2788	1230			1536	2996	1150	<b><u>3158</u></b>	<b><u>1090</u></b>		
526.blender_r	1536	1484	1580	<b><u>1487</u></b>	<b><u>1570</u></b>			1536	1287	1820	<b><u>1288</u></b>	<b><u>1820</u></b>		
527.cam4_r	1536	<b><u>2270</u></b>	<b><u>1180</u></b>	2230	1200			768	<b><u>1126</u></b>	<b><u>1190</u></b>	1103	1220		
538.imagick_r	1536	2213	1730	<b><u>2229</u></b>	<b><u>1710</u></b>			1536	2123	1800	<b><u>2130</u></b>	<b><u>1790</u></b>		
544.nab_r	1536	1263	2050	<b><u>1279</u></b>	<b><u>2020</u></b>			1536	1166	2220	<b><u>1169</u></b>	<b><u>2210</u></b>		
549.fotonik3d_r	1536	<b><u>4946</u></b>	<b><u>1210</u></b>	4944	1210			1536	4741	1260	<b><u>4743</u></b>	<b><u>1260</u></b>		
554.roms_r	1536	<b><u>5343</u></b>	<b><u>457</u></b>	5330	458			384	818	746	<b><u>820</u></b>	<b><u>744</u></b>		

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

Processes were assigned to specific processors using 'pbind' commands. The config file option 'submit' was used, along with a list of processors in the 'BIND' variable, to generate the pbind commands. (For details, please see the config file.)

## Operating System Notes

### Shell Environments:

ulimit -s 131072 was used to limit the space consumed by the stack (and therefore make more space available to the heap).

The "Logical Domains Manager" service was turned off using the command "svcadm disable ldmd".

### System Tunables:

(/etc/system parameters)

autoup = 86400

Causes pages older than the listed number of seconds to be written by fsflush.

doiflush = 0

Controls whether file system metadata syncs will be executed during fsflush invocations.

dopageflush = 0

Controls whether memory is examined for modified pages during fsflush invocations.

zfs:zfs\_arc\_max=1073741824

Determines the maximum size of the ZFS Adaptive Replacement Cache (ARC).



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** Nov-2017  
**Hardware Availability:** Apr-2017  
**Software Availability:** Jul-2017

## General Notes

The Building Block (BB) is just a Fujitsu SPARC M12-2S that is the basic unit to be expanded as if stacking up children's blocks.

File System:

tmpfs: output\_root was used to put run directories in /tmp/cpu2017  
zfs: operating system

Binaries were compiled on a system with 2x SPARC64 XII CPU + 1TB Memory using Oracle Solaris 11.3 SRU 24.4

## Platform Notes

Firmware Settings:

(XSCF operations)

Set High Speed Mode via XSCF command "sethsmode -s on".

Sysinfo program /export/cpu2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on H4S-115-D0 Thu Nov 30 20:17:18 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /usr/sbin/psrinfo

```
SPARC64-XII (chipid 0, clock 4250 MHz)
SPARC64-XII (chipid 1, clock 4250 MHz)
SPARC64-XII (chipid 10, clock 4250 MHz)
SPARC64-XII (chipid 11, clock 4250 MHz)
SPARC64-XII (chipid 12, clock 4250 MHz)
SPARC64-XII (chipid 13, clock 4250 MHz)
SPARC64-XII (chipid 14, clock 4250 MHz)
SPARC64-XII (chipid 15, clock 4250 MHz)
SPARC64-XII (chipid 2, clock 4250 MHz)
SPARC64-XII (chipid 3, clock 4250 MHz)
SPARC64-XII (chipid 4, clock 4250 MHz)
SPARC64-XII (chipid 5, clock 4250 MHz)
SPARC64-XII (chipid 6, clock 4250 MHz)
SPARC64-XII (chipid 7, clock 4250 MHz)
SPARC64-XII (chipid 8, clock 4250 MHz)
SPARC64-XII (chipid 9, clock 4250 MHz)
16 chips
1536 threads
4250 MHz
```

From kstat: 192 cores

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017

## Platform Notes (Continued)

From prtconf: 8899584 Megabytes

```
/etc/release:
  Oracle Solaris 11.3 SPARC
uname -a:
  SunOS H4S-115-D0 5.11 11.3 sun4v sparc sun4v
```

```
disk: df -h /export/cpu2017
Filesystem      Size      Used    Available Capacity  Mounted on
rpool/export    547G      10G      394G         3%      /export
```

(End of data from sysinfo program)

## Compiler Version Notes

=====  
CXXC 508.namd\_r(base) 510.parest\_r(base)

-----  
CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
CXXC 508.namd\_r(peak) 510.parest\_r(peak)

-----  
CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
CC 511.povray\_r(base) 526.blender\_r(base)

-----  
CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30  
cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
CC 511.povray\_r(peak) 526.blender\_r(peak)

-----  
CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30  
cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
FC 507.cactuBSSN\_r(base)

-----  
CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017

## Compiler Version Notes (Continued)

cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30  
f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30

=====  
FC 507.cactuBSSN\_r(peak)

=====  
CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30  
cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30  
f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30

=====  
CC 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)

=====  
cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30

=====  
CC 519.lbm\_r(peak) 538.imagick\_r(peak) 544.nab\_r(peak)

=====  
cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30

=====  
FC 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)

=====  
f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30

=====  
FC 503.bwaves\_r(peak) 549.fotonik3d\_r(peak) 554.roms\_r(peak)

=====  
f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30

=====  
CC 521.wrf\_r(base) 527.cam4\_r(base)

=====  
f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30  
cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30

=====  
CC 521.wrf\_r(peak) 527.cam4\_r(peak)

=====  
f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017

## Compiler Version Notes (Continued)

cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30  
-----

## Base Compiler Invocation

C benchmarks:

cc

C++ benchmarks:

CC

Fortran benchmarks:

f90

Benchmarks using both Fortran and C:

f90 cc

Benchmarks using both C and C++:

CC cc

Benchmarks using Fortran, C, and C++:

CC cc f90

## Base Portability Flags

503.bwaves\_r: -D\_FILE\_OFFSET\_BITS=64  
507.cactuBSSN\_r: -DSPEC\_NO\_C99\_MATH\_IN\_CXX -D\_FILE\_OFFSET\_BITS=64  
508.namd\_r: -D\_FILE\_OFFSET\_BITS=64  
510.parest\_r: -D\_FILE\_OFFSET\_BITS=64  
511.povray\_r: -D\_FILE\_OFFSET\_BITS=64  
519.lbm\_r: -D\_FILE\_OFFSET\_BITS=64  
521.wrf\_r: -D\_FILE\_OFFSET\_BITS=64  
526.blender\_r: -DSPEC\_NO\_ISFINITE -xchar=u -D\_FILE\_OFFSET\_BITS=64  
527.cam4\_r: -D\_FILE\_OFFSET\_BITS=64  
538.imagick\_r: -D\_FILE\_OFFSET\_BITS=64  
544.nab\_r: -D\_FILE\_OFFSET\_BITS=64  
549.fotonik3d\_r: -D\_FILE\_OFFSET\_BITS=64  
554.roms\_r: -D\_FILE\_OFFSET\_BITS=64



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Fujitsu**  
**Fujitsu SPARC M12-2S**

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** Nov-2017  
**Hardware Availability:** Apr-2017  
**Software Availability:** Jul-2017

## Base Optimization Flags

### C benchmarks:

```
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment_align=4M -xthroughput -xalias_level=std
```

### C++ benchmarks:

```
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment_align=4M -xthroughput -xalias_level=compatible -std=c++03  
-lfast
```

### Fortran benchmarks:

```
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment_align=4M -xthroughput
```

### Benchmarks using both Fortran and C:

```
-m32 -fast(cc) -fast(f95) -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment_align=4M -xthroughput -xalias_level=std
```

### Benchmarks using both C and C++:

```
-m32 -fast(CC) -fast(cc) -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment_align=4M -xthroughput -xalias_level=std  
-xalias_level=compatible -std=c++03 -lfast
```

### Benchmarks using Fortran, C, and C++:

```
-m32 -fast(CC) -fast(cc) -fast(f95) -xtarget=sparc64xii -xipo=2  
-xpagesize=4M -xsegment_align=4M -xthroughput -xalias_level=std  
-xalias_level=compatible -std=c++03 -lfast
```

## Base Other Flags

### C benchmarks:

```
-xjobs=8
```

### C++ benchmarks:

```
-xjobs=8
```

### Fortran benchmarks:

```
-xjobs=8
```

### Benchmarks using both Fortran and C:

```
-xjobs=8
```

### Benchmarks using both C and C++:

```
-xjobs=8
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017

## Base Other Flags (Continued)

Benchmarks using Fortran, C, and C++:  
-xjobs=8

## Peak Compiler Invocation

C benchmarks:  
cc

C++ benchmarks:  
CC

Fortran benchmarks:  
f90

Benchmarks using both Fortran and C:  
f90 cc

Benchmarks using both C and C++:  
CC cc

Benchmarks using Fortran, C, and C++:  
CC cc f90

## Peak Portability Flags

503.bwaves\_r: -D\_FILE\_OFFSET\_BITS=64  
507.cactuBSSN\_r: -DSPEC\_NO\_C99\_MATH\_IN\_CXX -DSPEC\_LP64  
508.namd\_r: -D\_FILE\_OFFSET\_BITS=64  
510.parest\_r: -D\_FILE\_OFFSET\_BITS=64  
511.povray\_r: -D\_FILE\_OFFSET\_BITS=64  
519.lbm\_r: -D\_FILE\_OFFSET\_BITS=64  
521.wrf\_r: -D\_FILE\_OFFSET\_BITS=64  
526.blender\_r: -DSPEC\_NO\_ISFINITE -xchar=u -D\_FILE\_OFFSET\_BITS=64  
527.cam4\_r: -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -D\_FILE\_OFFSET\_BITS=64  
549.fotonik3d\_r: -D\_FILE\_OFFSET\_BITS=64  
554.roms\_r: -D\_FILE\_OFFSET\_BITS=64





# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xO4  
-xtarget=sparc64xplus -xprefetch=latx:0.9  
-xprefetch_auto_type=indirect_array_access -xunroll=2  
-W2,-Afully_unroll:always=on -Wc,-Qiselect-funcalign=64
```

```
538.imagick_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xO4 -m64  
-xtarget=sparc64xplus -xinline_param=level:3  
-xprefetch=latx:0.7  
-xprefetch_auto_type=indirect_array_access -xunroll=4  
-Wc,-Qiselect-funcalign=4
```

```
544.nab_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xO4 -xunroll=3
```

C++ benchmarks:

```
508.namd_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xtarget=sparc64xplus  
-xalias_level=compatible -Wc,-Qms_pipe+alldoall -std=c++03
```

```
510.parest_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xtarget=sparc64xplus  
-xalias_level=compatible -xthroughput=no  
-xprefetch=no%auto -std=c++03
```

Fortran benchmarks:

```
503.bwaves_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xinline_param=level:1  
-xprefetch=latx:0.5
```

```
549.fotoni3d_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xthroughput=no  
-xprefetch=latx:0.8  
-xprefetch_auto_type=indirect_array_access -W2,-Rujam
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu  
Fujitsu SPARC M12-2S

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Nov-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017

## Peak Optimization Flags (Continued)

```
554.roms_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M
-xsegment_align=256M -xthroughput -xtarget=sparc64xplus
-xthroughput=no -xprefetch_auto_type=indirect_array_access
-xunroll=3 -W2,-Rujam -Wc,-Qiselect-rcpa=2
-Wc,-Qiselect-rsqrrta=2 -Wc,-Qiselect-rsqrrtalx=2
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32
-fast(cc) -fast(f95) -xtarget=sparc64xii -xipo=2
-xpagesize=256M -xsegment_align=256M -xthroughput
-xtarget=sparc64xplus
```

```
527.cam4_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32
-fast(cc) -fast(f95) -xtarget=sparc64xii -xipo=2
-xpagesize=256M -xsegment_align=256M -xthroughput -m64
-Wc,-Qiselect-rcpa=2 -Wc,-Qiselect-rsqrrta=2
-Wc,-Qiselect-rsqrrtalx=2
```

Benchmarks using both C and C++:

```
511.povray_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32
-fast(CC) -fast(cc) -xtarget=sparc64xii -xipo=2
-xpagesize=256M -xsegment_align=256M -xthroughput
-xtarget=sparc64xplus -xipo=1 -xalias_level=std
-xthroughput=no -xinline_param=level:3
-Wc,-Qiselect-rcpa=2 -W2,-Afully_unroll:always=on
-xalias_level=compatible -features=no%except
-features=no%rtti -Qoption iropt -Afully_unroll:always=on
-library=stlport4 -lfast
```

```
526.blender_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32
-fast(CC) -fast(cc) -xtarget=sparc64xii -xipo=2
-xpagesize=256M -xsegment_align=256M -xthroughput
-library=stlport4
```

Benchmarks using Fortran, C, and C++:

```
-xprofile=collect:./feedback -xprofile=use:./feedback -m32 -fast(CC)
-fast(cc) -fast(f95) -xtarget=sparc64xii -xipo=2 -xpagesize=256M
-xsegment_align=256M -xthroughput -m64 -Wc,-Qiselect-funcalign=4
-Qoption cg -Qiselect-funcalign=4 -library=stlport4
```



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Fujitsu**  
**Fujitsu SPARC M12-2S**

SPECrate2017\_fp\_base = 1250

SPECrate2017\_fp\_peak = 1520

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test Date:** Nov-2017  
**Hardware Availability:** Apr-2017  
**Software Availability:** Jul-2017

## Peak Other Flags

C benchmarks:

-xjobs=8

C++ benchmarks:

-xjobs=8

Fortran benchmarks:

-xjobs=8

Benchmarks using both Fortran and C:

-xjobs=8

Benchmarks using both C and C++:

-xjobs=8

Benchmarks using Fortran, C, and C++:

-xjobs=8

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Oracle-Developer-Studio12.6.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-M12-2S.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Oracle-Developer-Studio12.6.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-M12-2S.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2017-11-30 06:17:16-0500.

Report generated on 2018-10-31 14:10:52 by CPU2017 PDF formatter v6067.

Originally published on 2017-12-26.